

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Noriaki Sakamoto et al.                      Art Unit : 2823  
Serial No. : 09/970,013                                      Examiner : Brook Kabede  
Filed : October 2, 2001  
Title : METHOD OF MANUFACTURING CIRCUIT DEVICE

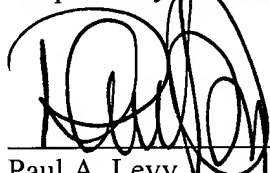
Commissioner for Patents  
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INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

  
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Date: December 29, 2003

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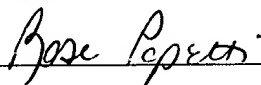
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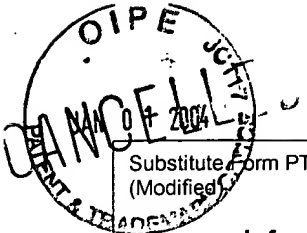
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(Modified)U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
10417-102001Application No.  
09/970,013**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant  
Noriaki Sakamoto et al.Filing Date  
October 2, 2001Group Art Unit  
2823**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	09/944,322	3/7/2002	Sakamoto et al.			
	AB	10/010,890	9/26/2002	Sakamoto et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

**Other Documents (include Author, Title, Date, and Place of Publication)**

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	AQ	
	AR	
	AS	
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.